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McKenna Long & Aldridge LLP 1900 K Street, NW Washington, D.C. 20006 Voice: (202) 496-7500 Fax: (202) 496-7756

Date: April 6, 2004

To:

Name/Company	Fax No.	Phone No.
Examiner S. H. Rao	(571) 273-1718	
U.S. PTO		

From:

Rebecca Goldman Rudich/E. Shavers

Phone: (202) 496-7463

Number of Pages (including cover): 6

Message:

U.S. Application No. 09/875,197; filed June 7, 2001

Inventor(s): Joon-Young YANG

METHOD OF FABRICATING A THIN FILM TRANSISTOR

	User No.	579	Client/Matter No.: 8733.423	
- 1				i

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PATENT/WASHINGTON

8733.132.20 File No.:

Attorney: RGR/es

MLA No.: 423

Application No.: 09/875,197

In the Matter of the Application of: Joon-Young YANG

METHOD OF FABRICATING A THIN FILM

For:

TRANSISTOR

Filed:

June 7, 2001

Date:

April 6, 2004

The following has been received in the U.S. Patent Office on the date stamped hereon:

1. Supplemental Amendment

DUE DATE: April 6, 2004

Docket No.: 8733.132.20-US

(PATENT)

Jule Stort

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Joon-Young Yang

Confirmation No.: 8761

Application No.: 09/875,197

Art Unit: 2814

Filed: June 7, 2001

Examiner: S. H. Rao

For: METHOD OF FABRICATING A THIN FILM

TRANSISTOR

Customer No.: 30827

SUPPLEMENTAL AMENDMENT

MS AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In response to the Examiner's Final Office Action dated December 31, 2003 (Paper No. 12), Applicant respectfully requests that the above-identified Patent application be reconsidered in view of the remarks.

List of the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.